

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Kannan Raj et al.

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Serial No.:

09/839,023

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Dalzid E. Singh

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MULTIPLE PROCESSORS

Assignee:

Intel Corporation

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicant submits the references listed on the attached form PTO 1449, copies of which are enclosed. A copy of a communication dated September 15, 2006 from a foreign patent office in a counterpart application is also enclosed. I, the undersigned, hereby certify that each item of information contained in the Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Information Disclosure Statement. Please apply any charges or credits to Deposit Account No. 20-1504 (ITL.0462US).

October 19, 2006

Date

Fred G. Fruner, Jr., Red. No. 40,779

Trop Pruner & Hu, P

Respectfully submitted

8554 Katy Freeway, Ste. 100

Houston, TX 77024

713/468-8880

713/468-8883 [fax]

Attorney for Intel Corporation

Customer No.

21906

Date of Deposit: October 19, 2006

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450,

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INITIAL	_	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	IF APPROPRIAT	
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